



# Model Curriculum

**QP Name: Embedded Product Design - Technical Lead**

**QP Code: ELE/Q1403**

**QP Version: 4.0**

**NSQF Level: 5.5**

**Model Curriculum Version: 4.0**

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## Training Parameters

<b>Sector</b>	Electronics
<b>Sub-Sector</b>	Semiconductor & Components
<b>Occupation</b>	Product Design-S&C
<b>Country</b>	India
<b>NSQF Level</b>	5.5
<b>Aligned to NCO/ISCO/ISIC Code</b>	NCO-2015/2512.0501
<b>Minimum Educational Qualification and Experience</b>	<p>Completed 4 year UG program (Physics/Electronics/Electrical/Computer Science/Mechanical) with 1.5 years of experience Relevant Experience</p> <p>OR</p> <p>Completed 3 year UG degree (Physics/Electronics/Electrical/Computer Science/Mechanical) with 1.5 years of experience Relevant Experience</p> <p>OR</p> <p>Completed 3 year diploma after 10th (Electronics/Electrical/Computer Science/Mechanical) with 3 years of Relevant Experience</p> <p>OR</p> <p>Previous relevant Qualification of NSQF Level (5) with 1.5 years of Relevant Experience</p> <p># Relevant Experience in Semiconductor &amp; Components.</p>
<b>Pre-Requisite License or Training</b>	NA
<b>Minimum Job Entry Age</b>	18 Years
<b>Last Reviewed On</b>	01.05.2025
<b>Next Review Date</b>	30.04.2028
<b>NSQC Approval Date</b>	08.05.2025
<b>QP Version</b>	4.0
<b>Model Curriculum Creation Date</b>	01.05.2025
<b>Model Curriculum Valid Up to Date</b>	30.04.2028
<b>Model Curriculum Version</b>	4.0
<b>Minimum Duration of the Course</b>	600 Hours
<b>Maximum Duration of the Course</b>	600 Hours

## Program Overview

This section summarizes the end objectives of the program along with its duration.

### Training Outcomes

At the end of the program, the learner should have acquired the listed knowledge and skills:

- Describe the process of designing embedded electronic products.
- Demonstrate the process of developing and testing software solutions for embedded products.
- Describe the process of testing and rectifying malfunctions in the prototype of the embedded product.
- Demonstrate the process of testing and rectifying malfunctions in the prototype of the embedded product.
- Explain the importance of working effectively at the workplace.
- Demonstrate various practices to be followed to maintain health and safety at work.

### Compulsory Modules

The table lists the modules and their duration corresponding to the Compulsory NOS of the QP.

NOS and Module Details	Theory Duration	Practical Duration	On-the-Job Training Duration (Recommended)	On-the-Job Training Duration (Mandatory)	Total Duration
<b>ELE/N1432: Create Designs for Embedded Electronic product</b>	60:00	60:00	00:00	60:00	180:00
Module 1: Process of Creating the designs for Embedded Electronic product	60:00	60:00	00:00	60:00	180:00
<b>ELE/N1433: Develop, Debug, and Verify Software for Embedded Products</b>	30:00	90:00	00:00	60:00	180:00
Module 2: Process of Developing , Debugging, and Verify Software for Embedded Products	30:00	90:00	00:00	60:00	180:00
<b>ELE/N1434: Perform testing and rectify malfunctions in the prototype of the embedded product</b>	66:00	54:00	00:00	60:00	180:00
Module 3: Process of performing testing and rectify malfunctions in the prototype of the embedded product	66:00	54:00	00:00	60:00	180:00
<b>DGT/VSQ/N0102- Employability Skills (60Hours)</b>	24:00	36:00	00:00	00:00	60:00
Module 4: Employability Skills (60 Hours)	24:00	36:00	00:00	00:00	60:00
<b>Total Duration</b>	<b>180:00</b>	<b>240:00</b>	<b>00:00</b>	<b>180:00</b>	<b>600:00</b>

# Module Details

## Module 1: Process of Creating the designs for Embedded Electronic product Mapped to ELE/N1432

### Terminal Outcomes:

- Describe the process of preparing and developing the design for embedded products.
- Describe the process of using the appropriate design techniques.
- Demonstrate the process of performing post design activities.

Duration: 60:00	Duration: 60:00
Theory – Key Learning Outcomes	Practical – Key Learning Outcomes
<ul style="list-style-type: none"> <li>• Interpret the design requirement and prepare a detail list of requirement-specification including software, hardware and firmware as per given specifications and system architecture</li> <li>• Evaluate the feasibility of the design and prepare a feasibility report of the new product design</li> <li>• Analyse datasheets of components such as ageing, caveats, constraints like temperature/ pressure affecting normal operational characteristics of the product</li> <li>• List proper software for designing and testing as per the company standards and requirement</li> <li>• Apply the processes and approved techniques involved in embedded product designing as per stipulated quality standards and compliances</li> <li>• Evaluate the design to ensure that the product complies with relevant safety standards, performance and budget requirements</li> <li>• Evaluate supplier/vendor documentation and apply provided suggested courses of action for all hardware-related components</li> <li>• Identify the issues with relevant policy and procedure and report them to appropriate authority</li> </ul>	<ul style="list-style-type: none"> <li>• Use tools and hardware such as Computer, cross compiler, circuit simulator, device driver, PCB design software/ component library, microprocessor/ microcontroller and other peripherals</li> <li>• Use testing devices such as oscilloscopes, multi meters, JTAG, Traffic generators/analyzers, signal generators/analyzers, frequency counters, power supply</li> <li>• Use operating system and language required for developing the new product design and calibrate them</li> <li>• Develop a work plan and create a design document both high-level and low-level</li> <li>• Use unit-test cases (UTCs) to test the product according to white box testing method</li> <li>• Assess that the prototype devices or circuits are built as per required specifications and conduct performance test</li> <li>• Compare the code with the design document to ensure that it maps to the requirements</li> <li>• Prepare a document containing hardware specifications, design documentation, hardware-related detailed design documentation, BOMs and Parts Lists, verification tests and reports</li> <li>• Comply with IP confidentiality rights and configuration management of</li> </ul>

	hardware items
<b>Classroom Aids</b>	
Training kit (Trainer guide, Presentations). Whiteboard, Marker, projector, laptop	
<b>Tools, Equipment and Other Requirements</b>	
Computer, cross compiler, circuit simulator, device driver PCB design software/ component library, microprocessor/ microcontroller and other peripherals, PCB designing software/ spice, Laptop and desktop computers with C, C++, embedded C, PYTHON installed, Windows, iOS, Linux, virtual machines, Embedded device with RTOS, Symbian, Android or Linux, Oscilloscopes, multi meters, JTAG, Traffic generators/analyzers, signal, generators/ analyzers, frequency counters, power supply, Sample BOM	

## Module 2: Process of Developing, Debugging, and Verify Software for Embedded Products

*Mapped to ELE/N1433*

### Terminal Outcomes:

- Demonstrate the process of preparing for embedded product software development based on design documents.
- Demonstrate the process of developing software solutions for embedded products.
- Demonstrate the process of testing the software solutions for embedded products.

Duration: 30:00	Duration: 90:00
Theory – Key Learning Outcomes	Practical – Key Learning Outcomes
<ul style="list-style-type: none"> <li>• Identify extent of code development work required and plan activities accordingly to meet scheduled timelines</li> <li>• Identify and use software configuration management tools, editor and compiler</li> <li>• List the range of code generation tools and unit testing tools used to develop software code</li> <li>• Identify the components which are suitable for re-use and also different sources of information for writing software code</li> <li>• Apply the principles of embedded software programming and real-time programming, best practices for embedded programming and current practice in the infrastructure design of software code</li> <li>• Identify the syntax and semantics of the C language for embedded programming, how to access memory-mapped peripherals using C, how to write interrupt handlers in C</li> <li>• List software requirements specifications including functionality, performance and design constraints</li> <li>• Implement the key features of the programming language such as use of registers, addressing modes, assembler instructions, subroutines and flags and so on</li> <li>• Evaluate the working of the product</li> </ul>	<ul style="list-style-type: none"> <li>• Use appropriate development kit, software and microprocessor or micro controller based on specified requirements and performance standard</li> <li>• Write efficient, readable and maintainable software code using coding tools, considering purpose of the software solution, type of architecture, potential challenges and how to deal with them</li> <li>• Test the program by compiling and running the programs to check working of basic input/ output functions, memory organization, peripheral operations (e.g., timers, data ports, etc.) and interrupt operation</li> <li>• Apply the process for converting technical specifications into code</li> <li>• Select embedded product development board and compiler/ development environment for the microcontroller/ processor to be programmed</li> <li>• Create and modify code using embedded product peripherals such as timers, data communication ports and so on</li> <li>• Develop microprocessor/ microcontroller software to comply with a specified function and operating parameters</li> <li>• Use digital signal processors and field</li> </ul>



<p>by loading the compiled code in to product</p>	<p>programmable gate arrays (FGPA) firmware code development</p> <ul style="list-style-type: none"> <li>• Develop applications for signal processing, data acquisition, event processing and data management and communication functions</li> <li>• Develop systems using real-time embedded operating systems</li> <li>• Create UTCs, execute them and fix the issues</li> <li>• Apply the steps for testing such as developing testing procedures, analysing inputs from appropriate people, recording corrective actions for identified defects, submitting tested code for approval</li> <li>• Use defect tracking tools</li> <li>• Apply debugging techniques to fix the identified issue in the embedded code using assembler/ simulator software packages, emulators, debuggers and so on</li> </ul>
<b>Classroom Aids</b>	
<p>Training kit (Trainer guide, Presentations). Whiteboard, Marker, projector, laptop</p>	
<b>Tools, Equipment and Other Requirements</b>	
<p>Computer, cross compiler, circuit simulator, device driver, Laptop and desktop computers with C, C++, embedded C, PYTHON installed, Windows, iOS, Linux, virtual machines, Embedded device with RTOS, Symbian, Android or Linux, Oscilloscopes, multi meters, JTAG, Traffic generators/ analyzers, signal generators/analyzers, frequency counters, power supply</p>	



## Module 3: Process of performing testing and rectify malfunctions in the prototype of the embedded product

*Mapped to ELE/N1434*

### Terminal Outcomes:

- Demonstrate the process of testing the prototype of the embedded product.
- Demonstrate the process of rectifying malfunction, if any, in the prototype.

Duration: 66:00	Duration: 54:00
Theory – Key Learning Outcomes	Practical – Key Learning Outcomes
<ul style="list-style-type: none"> <li>• Identify the malfunctions in the prototype devices/ components</li> <li>• List the software and hardware used to test malfunctions</li> <li>• Identify the applications of debugging</li> <li>• Identify and resolve malfunctions as per standard operating system</li> <li>• Evaluate end-of-life and component change notifications and sourcing alternate components</li> </ul>	<ul style="list-style-type: none"> <li>• Use debugging to analyse and resolve malfunctions in the prototype</li> <li>• Apply approved techniques to check defects/ malfunctions</li> <li>• Analyse unit failures and develop corrective actions</li> <li>• Assess the techniques used for electromagnetic interference or compatibility (EMI/ EMC) testing, thermal testing, scale testing, rate test. KPI test etc.</li> <li>• Demonstrate working with the testing/QA team to fix identified problems</li> <li>• Comply with the quality standards defined by the organization</li> <li>• Prepare a document containing the completed new product design work and submit to relevant authority/ person for approval</li> </ul>
Classroom Aids	
Training kit (Trainer guide, Presentations)	
Tools, Equipment and Other Requirements	
Computer, cross compiler, circuit simulator, device driver, Laptop and desktop computers with C, C++, embedded C, PYTHON installed, Windows, iOS, Linux, virtual machines, Embedded device with RTOS, Symbian, Android or Linux, Oscilloscopes, multi meters, JTAG, Traffic generators/ analyzers, signal generators/analyzers, frequency counters, power supply	

## Module 3: Employability Skills (60 Hours)

Mapped to DGT/VSQ/N0102

### Terminal Outcomes:

- Discuss about Employability Skills in meeting the job requirements
- Describe opportunities as an entrepreneur.
- Describe ways of preparing for apprenticeship & Jobs appropriately.

Duration: 24:00	Duration: 36:00
Theory – Key Learning Outcomes	Practical – Key Learning Outcomes
<ul style="list-style-type: none"> <li>• Explain constitutional values, civic rights, responsibility towards society to become a responsible citizen</li> <li>• Discuss 21<sup>st</sup> century skills</li> <li>• Explain use of basic English phrases and sentences.</li> <li>• Demonstrate how to communicate in a well-behaved manner</li> <li>• Demonstrate how to work with others</li> <li>• Demonstrate how to operate digital devices</li> <li>• Discuss the significance of Internet and Computer/ Laptops</li> <li>• Discuss the need for identifying business opportunities</li> <li>• Discuss about types of customers.</li> <li>• Discuss on creation of biodata</li> <li>• Discuss about apprenticeship and opportunities related to it.</li> </ul>	<ul style="list-style-type: none"> <li>• List different learning and employability related GOI and private portals and their usage</li> <li>• Show how to practice different environmentally sustainable practices.</li> <li>• Exhibit 21st century skills like Self-Awareness, Behavior Skills, time management, etc.</li> <li>• Show how to use basic English sentences for everyday conversation in different contexts, in person and over the telephone</li> <li>• Demonstrate how to communicate in a well-mannered way with others.</li> <li>• Demonstrate how to communicate effectively using verbal and nonverbal communication etiquette</li> <li>• Utilize virtual collaboration tools to work effectively</li> <li>• Demonstrate how to maintain hygiene and dressing appropriately.</li> <li>• Perform a mock interview</li> </ul>
Classroom Aids	
Training Kit (Trainer Guide, Presentations). Whiteboard, Marker, Projector, Laptop	
Tools, Equipment and Other Requirements	
Computer, UPS, Scanner, Computer Tables, LCD Projector, Computer Chairs, White Board OR Computer Lab	

## Module 4: On-the-Job Training

### Mapped to Embedded Product Design - Technical Lead

<b>Mandatory Duration: 180:00</b>	<b>Recommended Duration: 00:00</b>
<b>Location: On Site</b>	
<p><b>Terminal Outcomes</b></p> <ol style="list-style-type: none"> <li>1. Explain the process of preparing a feasibility report of the new product design.</li> <li>2. Use testing devices such as oscilloscopes, multi meters, JTAG, Traffic generators/analyzers, signal generators/analyzers, frequency counters, power supply</li> <li>3. Analyse datasheets of components such as ageing, caveats, constraints like temperature/ pressure affecting normal operational characteristics of the product</li> <li>4. Write efficient, readable and maintainable software code using coding tools.</li> <li>5. Create UTCs, execute them and fix the issues</li> <li>6. Analyse unit failures and develop corrective actions</li> <li>7. Create a sample feedback form to obtain feedback from customers, colleagues etc.</li> <li>8. Administer first aid in case of a minor accident.</li> <li>9. Demonstrate how to maintain a healthy, safe and secure working environment.</li> </ol>	

# Annexure

## Trainer Requirements

Trainer Prerequisites						
Minimum Educational Qualification	Specialization	Relevant Industry Experience		Training Experience		Remarks
		Years	Specialization	Years	Specialization	
Diploma/ITI/ Degree	Electronics /Electrical /Computer Science /Mechanical	4	Semiconductor, Components Domain	1 year	Electronics & Semiconductors Domain	

Trainer Certification	
Domain Certification	Platform Certification
“Embedded Product Design - Technical Lead”, “ELE/Q1403,v4.0”, Minimum accepted score is 80%	Recommended that the Trainer is certified for the “Embedded Product Design - Technical Lead” Trainer (VET and Skills), mapped to the Qualification Pack: “MEP/Q2601, V2.0”, with minimum score of 80%

## Assessor Requirements

Assessor Prerequisites						
Minimum Educational Qualification	Specialization	Relevant Industry Experience		Training/Assessment Experience		Remarks
		Years	Specialization	Years	Specialization	
Diploma/ ITI/ Degree	Electronics /Electrical /Computer Science /Mechanical	5	Semiconductor, Components Domain	2 years preferably	Electronics & Semiconductors Domain	

Assessor Certification	
Domain Certification	Platform Certification
“Embedded Product Design - Technical Lead”, “ELE/Q1403, v4.0”, Minimum accepted score is 80%	Recommended that the Assessor is certified for the “Embedded Product Design - Technical Lead” Assessor (VET and Skills), mapped to the Qualification Pack: “MEP/Q2701, V2.0”, with minimum score of 80%

## Assessment Strategy

### 1. Assessment System Overview:

- Batches assigned to the assessment agencies for conducting the assessment on SDMS/SIP or email
- Assessment agencies send the assessment confirmation to VTP/TC looping SSC
- The assessment agency deploys the ToA certified Assessor for executing the assessment
- SSC monitors the assessment process & records

### 2. Testing Environment

To ensure a conducive environment for conducting a test, the trainer will:

- Confirm that the center is available at the same address as mentioned on SDMS or SIP
- Check the duration of the training.
- Check the Assessment Start and End time to be 10 a.m. and 5 p.m. respectively
- Ensure there are 2 Assessors if the batch size is more than 30.
- Check that the allotted time to the candidates to complete Theory & Practical Assessment is correct.
- Check the mode of assessment—Online (TAB/Computer) or Offline (OMR/PP).
- Confirm the number of TABs on the ground are correct to execute the Assessment smoothly.
- Check the availability of the Lab Equipment for the particular Job Role.

### 3. Assessment Quality Assurance levels / Framework:

- Question papers created by the Subject Matter Experts (SME)
- Question papers created by the SME verified by the other subject Matter Experts
- Questions are mapped with NOS and PC
- Question papers are prepared considering that level 1 to 3 are for the unskilled & semi-skilled individuals, and level 4 and above are for the skilled, supervisor & higher management
- The assessor must be ToA certified and the trainer must be ToT Certified
- The assessment agency must follow the assessment guidelines to conduct the assessment

### 4. Types of evidence or evidence-gathering protocol:

- Time-stamped & geotagged reporting of the assessor from assessment location
- Centre photographs with signboards and scheme-specific branding
- Biometric or manual attendance sheet (stamped by TP) of the trainees during the training period
- Time-stamped & geotagged assessment (Theory + Viva + Practical) photographs & videos

### 5. Method of verification or validation:

To verify the details submitted by the training center, the assessor will undertake:

- A surprise visit to the assessment location
- A random audit of the batch
- A random audit of any candidate

### 6. Method for assessment documentation, archiving, and access

To protect the assessment papers and information, the assessor will ensure:

- Hard copies of the documents are stored

- Soft copies of the documents & photographs of the assessment are uploaded / accessed from Cloud Storage
- Soft copies of the documents & photographs of the assessment are stored on the Hard drive



# References

## Glossary

Term	Description
<b>Declarative knowledge</b>	Declarative knowledge refers to facts, concepts and principles that need to be known and/or understood in order to accomplish a task or to solve a problem.
<b>Key Learning</b>	Key learning outcome is the statement of what a learner needs to know, understand and be able to do in order to achieve the terminal outcomes. A set of key learning outcomes will make up the training outcomes. Training outcome is specified in terms of knowledge, understanding (theory) and skills (practical application).
<b>OJT (M)</b>	On-the-job training (Mandatory); trainees are mandated to complete specified hours of training on site
<b>OJT (R)</b>	On-the-job training (Recommended); trainees are recommended the specified hours of training on site
<b>Procedural Knowledge</b>	Procedural knowledge addresses how to do something, or how to perform a
<b>Training Outcome</b>	Training outcome is a statement of what a learner will know, understand and be able to do <b>upon the completion of the training</b> .
<b>Terminal Outcome</b>	Terminal outcome is a statement of what a learner will know, understand and be able to do <b>upon the completion of a module</b> . A set of terminal outcomes help to achieve the training outcome.

## Acronyms and Abbreviations

Term	Description
DC	Direct Current
ISO	International Organization for Standardization
NCO	National Occupational Standards
NOS	National Skills Qualification Committee
NSQF	National Skills Qualification Framework
OJT	On-the-Job Training
OMR	Optical Mark Recognition
PC	Performance Criteria
PwD	Persons with Disabilities
QP	Qualification Pack
SDMS	Skill Development & Management System
SIP	Skill India Portal
SME	Small and Medium Enterprises
SOP	Standard Operating Procedure
SSC	Sector Skill Council
TC	Trainer Certificate
ToA	Training of Assessors
ToT	Training of Trainers
TP	Training Provider